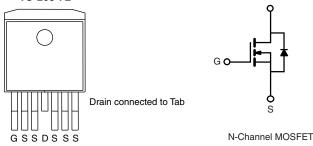


# Automotive N-Channel 40 V (D-S) 175 °C MOSFET

D

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	40			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 V$	0.0017			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 V$	0.0020			
I <sub>D</sub> (A)	200			
Configuration	Single			

#### TO-263-7L



#### FEATURES

- TrenchFET<sup>®</sup> Power MOSFET
- Package with Low Thermal Resistance
- 100 %  $R_q$  and UIS Tested
- AEC-Q101 Qualified<sup>d</sup>
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>



ORDERING INFORMATION		
Package	TO-263-7L	
Lead (Pb)-free and Halogen-free	SQM200N04-1m7L-GE3	

ABSOLUTE MAXIMUM RATINGS	(T <sub>C</sub> = 25 °C, unles	s otherwise noted	4)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V <sub>DS</sub>	40	N	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
Continuous Drain Current	T <sub>C</sub> = 25 °C <sup>a</sup>	1	200		
	T <sub>C</sub> = 125 °C	I <sub>D</sub>	193		
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	200	А	
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	600		
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	95		
Single Pulse Avalanche Energy		E <sub>AS</sub>	451	mJ	
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	P	375	W	
	T <sub>C</sub> = 125 °C	P <sub>D</sub>	125		
Operating Junction and Storage Temperature F	lange	T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	PCB Mount <sup>c</sup>	R <sub>thJA</sub>	40	°C/W	
Junction-to-Case (Drain)		R <sub>thJC</sub>	0.4	C/W	

#### Notes

- a. Package limited.
- b. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.

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## SQM200N04-1m7L



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static				•				
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = 250 \mu A$		40	-	-	v	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$		1.5	2.0	2.5		
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$		-	-	± 100	nA	
		$V_{GS} = 0 V$	V <sub>DS</sub> = 40 V	-	-	1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{GS} = 0 V$	$V_{DS} = 40 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$	-	-	50	μA	
		$V_{GS} = 0 V$	$V_{DS} = 40 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$	-	-	250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	$V_{DS} \ge 5 V$	200	-	-	Α	
Drain-Source On-State Resistance <sup>a</sup>		$V_{GS} = 10 V$	I <sub>D</sub> = 30 A	-	0.0012	0.0017	Ω	
	в	$V_{GS} = 10 V$	$I_D = 30 \text{ A},  \text{T}_\text{J} = 125 \ ^\circ\text{C}$	-	-	0.0028		
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 30 A, T <sub>J</sub> = 175 °C	-	-	0.0034		
		$V_{GS} = 4.5 V$	I <sub>D</sub> = 20 A	-	0.0014	0.0020		
Forward Transconductanceb	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A		-	181	-	S	
Dynamic <sup>b</sup>								
Input Capacitance	C <sub>iss</sub>		V <sub>DS</sub> = 20 V, f = 1 MHz	-	8934	11 168	pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$		-	1592	1990		
Reverse Transfer Capacitance	C <sub>rss</sub>			-	928	1160		
Total Gate Charge <sup>c</sup>	Qg		$V_{DS} = 20 \text{ V}, \text{ I}_{D} = 20 \text{ A}$	-	194	291	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V		-	25	-		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			-	40	-		
Gate Resistance	R <sub>g</sub>	f = 1 MHz		0.4	0.8	1.2	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			-	22	33		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 20 \text{ V}, \text{ R}_{L} = 1 \Omega$ $\text{I}_{D} \cong 20 \text{ A}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_{g} = 1 \Omega$		-	17	26	- ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	70	105		
Fall Time <sup>c</sup>	t <sub>f</sub>			-	16	24		
Source-Drain Diode Ratings and Char	acteristics <sup>b</sup>				•			
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	600	Α	
Forward Voltage	V <sub>SD</sub>	I <sub>F</sub> = 60 A, V <sub>GS</sub> = 0 V		-	0.8	1.5	V	

Notes

a. Pulse test; pulse width  $\leq 300~\mu\text{s},$  duty cycle  $\leq 2~\%.$ 

b. Guaranteed by design, not subject to production testing.

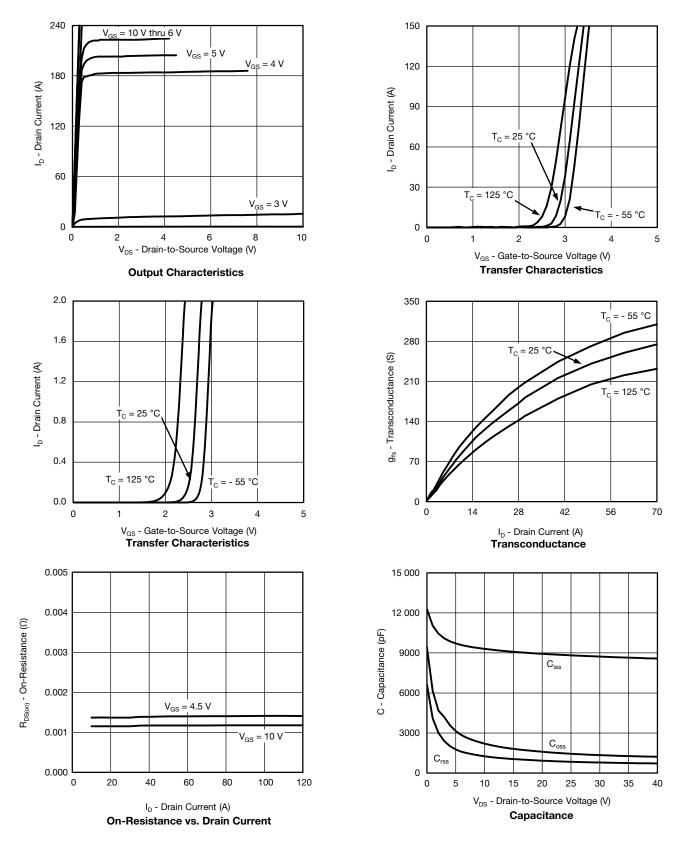
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





### **TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



S12-1902-Rev. A, 13-Aug-12

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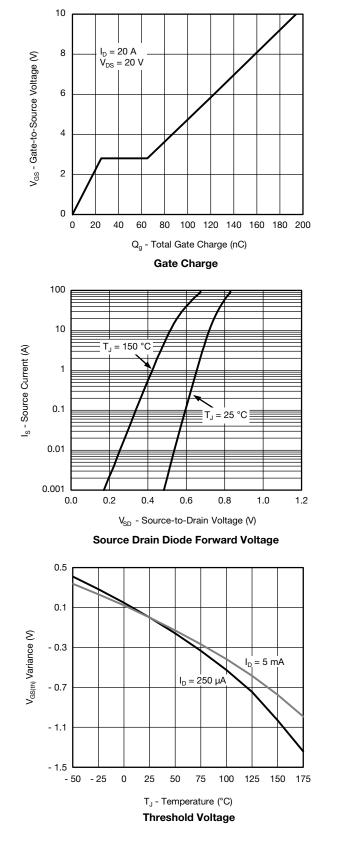
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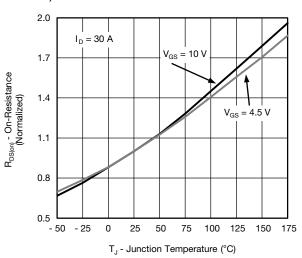
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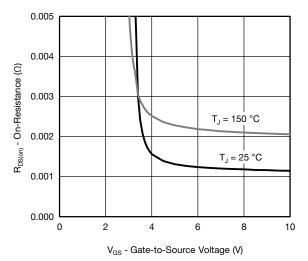


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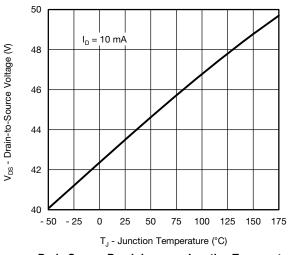








**On-Resistance vs. Gate-to-Source Voltage** 



Drain Source Breakdown vs. Junction Temperature

S12-1902-Rev. A, 13-Aug-12

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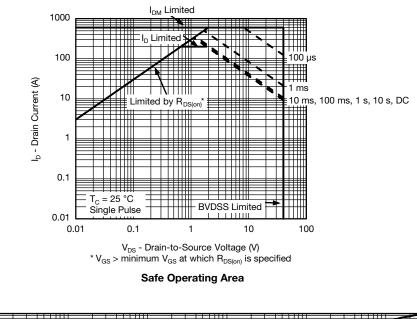
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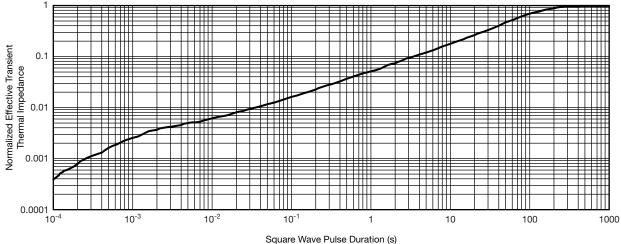


SQM200N04-1m7L

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### **THERMAL RATINGS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)

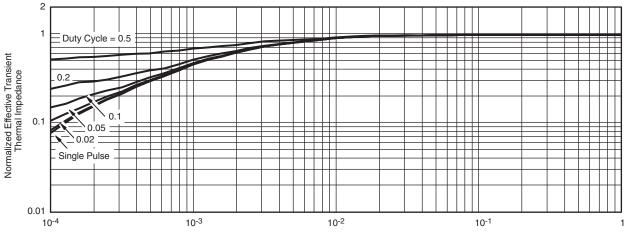




Normalized Thermal Transient Impedance, Junction-to-Ambient



### **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



Square Wave Pulse Duration (s)

Normalized Thermal Transient Impedance, Junction-to-Case

#### Note

The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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